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125-CS (P139US)

SEP 18 2002

PATENT
U.S. Ser. No. 09/821,897

44 C185
10/02/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Zheng

Serial No: 09/821,897

Filing Date: March 30, 2001

Art Unit: 2183

Examiner: unassigned

For: SYSTEM ON A CHIP WITH MULTIPLE POWER PLANES AND
ASSOCIATE POWER MANAGEMENT METHODS

RECEIVED

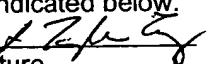
Hon. Commissioner of Patents
Washington, D. C. 20231.

SEP 23 2002

Technology Center 2100

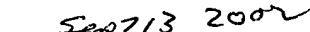
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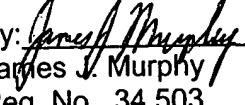
REQUEST FOR STATUS OF APPLICATION

Applicants hereby request the current status of the above-identified application for patent. While several papers have been filed with the U.S. Patent and Trademark Office, the last paper received from the U.S. Patent and Trademark Office was the official recordal of assignment documents mailed October 31, 2001. No further correspondence has been received.

It is our understanding that the U.S. Patent and Trademark Office has a policy of responding to Requests for Status within thirty (30) days of receipt. Accordingly, please respond prior to October 20, 2002.

If the Examiner has any questions or comments concerning this paper or the present parent and/or divisional application in general, the Examiner is invited to call the undersigned at (214) 745-5374.

Respectfully submitted,
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Date Sept. 13, 2002